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Concise Explanation of Relevance

93-1363 discloses an enable detection circuit 3 which selects data from a logic circuit 1 by using a shift signal SH. Relevance to the claimed invention, in particular to the claimed enable signal, is merely found in the fact that a signal for selecting data from the logic circuit is named "enable" signal.

95-19824 shows that a timing control signal is generated from a data signal, and vertical and horizontal synchronizing signals. The art disclosed in this document is the same as the prior art described in the specification and drawings of the present application.